Amendments to the Claims:

This listing of claims will replace all prior versions and listings of claims in the application:

Listing of the Claims:

1.   (Currently Amended) A method comprising:

determining a <u>power state of a first system, the power state to be one of at least a first and second power states,</u> ~~first system Advanced Configuration and Power Interface Specification (ACPI) state, the ACPI states including a first power on state, a second power state, and a power off state,~~ the second power state to consume less power than the first power state; and

<u>in response to the system being in the second power state,</u> switching a serial Advanced Technology Attachment (SATA) <u>link from the first system to a link with an autonomous subsystem</u>~~between two devices, the switching based upon the ACPI state.~~

2.   (Currently Amended) The method according to claim 1, wherein the <u>power state is power state of an Advanced Configuration Power Interface Specification (ACPI S) state</u> ~~is selected from the group consisting of S0, S1, S2, S3, S4, and S5.~~

3.   (Canceled)

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4.   (Canceled)

5.   (Currently Amended) The method according to claim 24, wherein:

if the ACPI state is S0, S1, or S2 then the SATA is switched to the first

system; and

if the ACPI state is S3, S4, or S5 then the SATA is switched to the

subsystem.

6.   (Currently Amended) The method according to claim 24, wherein:

if the ACPI state is S0, or S1 then the SATA is switched to the first system;

and

if the ACPI state is S2, S3, S4, or S5 then the SATA is switched to the

subsystem.

7.   (Currently Amended) A machine-readable medium having stored

thereon instructions, which when executed by a processor, causes said processor

to perform the following:

determine a power state of a first system, the power state to be at least one

of a first and second power states, a first system Advanced Configuration and

Power Interface Specification (ACPI) state, the ACPI states including a first

power on state, a second power state, and a power off state, the second power

state to consume less power than the first power state; and

in response to the system being in the second power state, switch a serial

Advanced Technology Attachment (SATA) link from the first system to a link

with an autonomous subsystem~~between two devices, the switching based upon~~

~~the ACPI state~~.

8.   (Canceled)

9.   (Currently Amended) A system comprising:

a serial Advanced Technology Attachment (SATA) device connected to a

switch;

a switch to connect the system to the SATA device when the system is in a

first power state, and the switch to connect an autonomous subsystem to the

SATA device when the system is in a second power state, the second power state

to conusme less power than the first power state.

~~a first system to connect to the SATA device through the switch; and~~

~~a subsystem to connect to the SATA device through the switch; the switch~~

~~to switch between the first system and the subsystem based on an Advanced~~

~~Configuration and Power Interface Specification (ACPI) state, the ACPI states~~

~~including a first power on state, a second power state, and a power off state, the~~

~~second power state to consume less power than the first power state.~~

10.   (Currently Amended) The system of claim 9, wherein the switch connecting the SATA device does not connect both the ~~first~~ system and the subsystem to the SATA device simultaneously.

11.   (Currently Amended) The system of claim 9, wherein the switch operation is controlled by signals from the ~~first~~ system.

12.   (Currently Amended) An apparatus comprising:

        means for determining a <u>power state of a first system, the power state to be one of at least a first and second power states, the second power state to consume less power than the first power state</u> ~~first system Advanced Configuration and Power Interface Specification (ACPI) state, the ACPI states including a first power on state, a second power state, and a power off state, the second power state to consume less power than the first power state~~; and

        means for switching a serial Advanced Technology Attachment (SATA) <u>link from the first system to a link with an autonomous subsystem in response to determining the first system is in the second power state</u> ~~between two devices based upon the ACPI state~~.

13.   (Original) The apparatus of claim 12, wherein means for switching further comprises a mutually exclusive switching means to a plurality of destinations.

14.   (Currently Amended) The apparatus of claim 12, wherein the <u>power state</u> <u>is power state of an Advanced Configuration Power Interface Specification</u> (ACPI) state ~~is selected from the group consisting of S0, S1, S2, S3, S4, and S5~~.

15.   (Previously Presented) The apparatus of claim 12, wherein the means for switching the SATA determined whether to switch based upon signals from the first system.